Fig.1

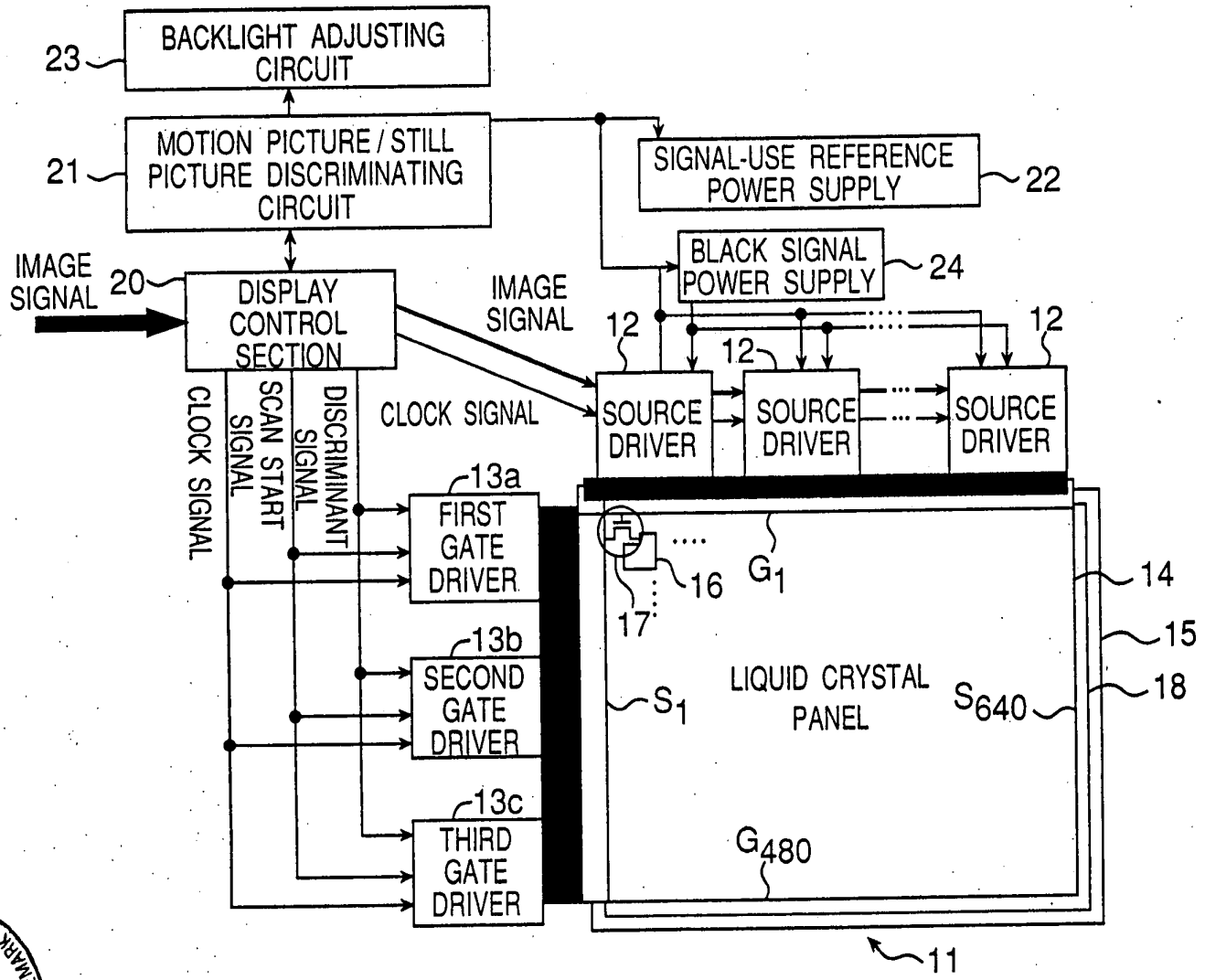


Fig.2

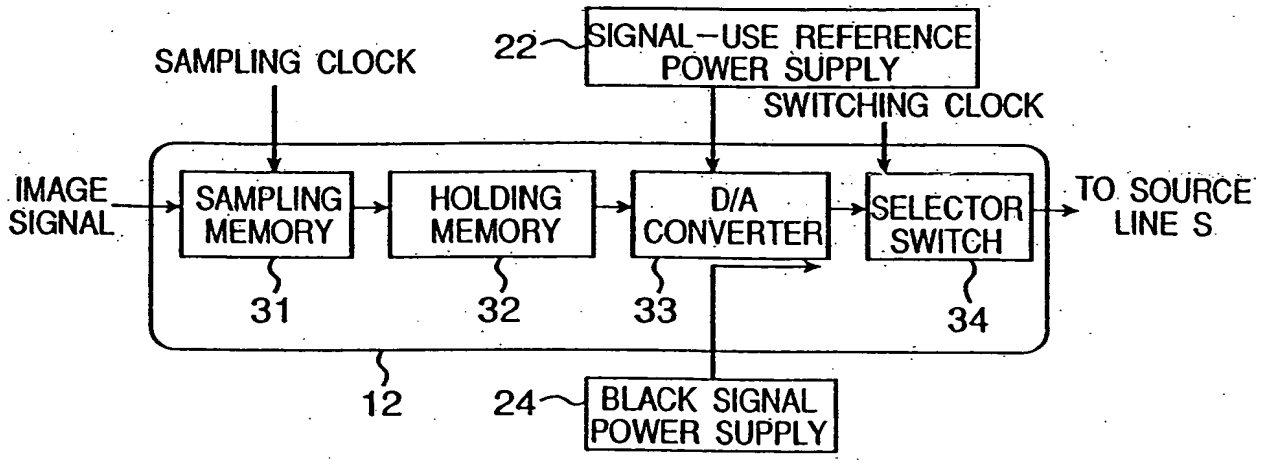


Fig.3

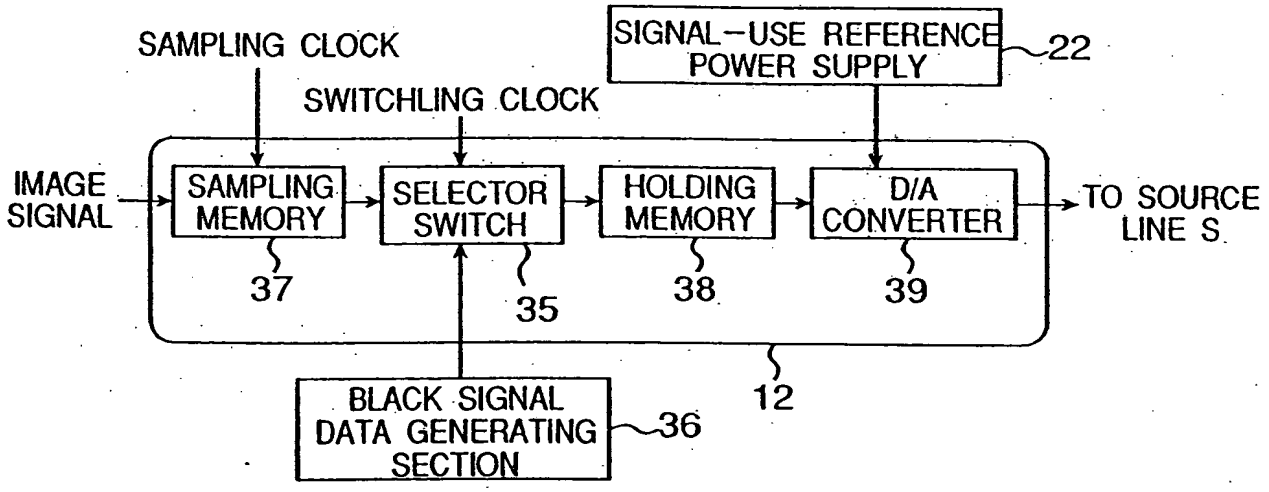


Fig.4

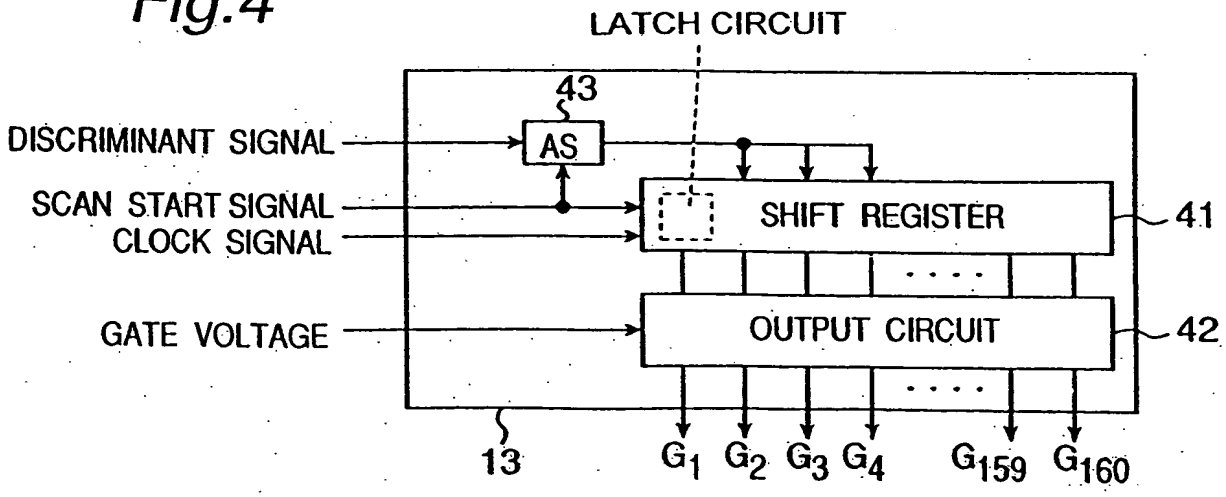


Fig.5

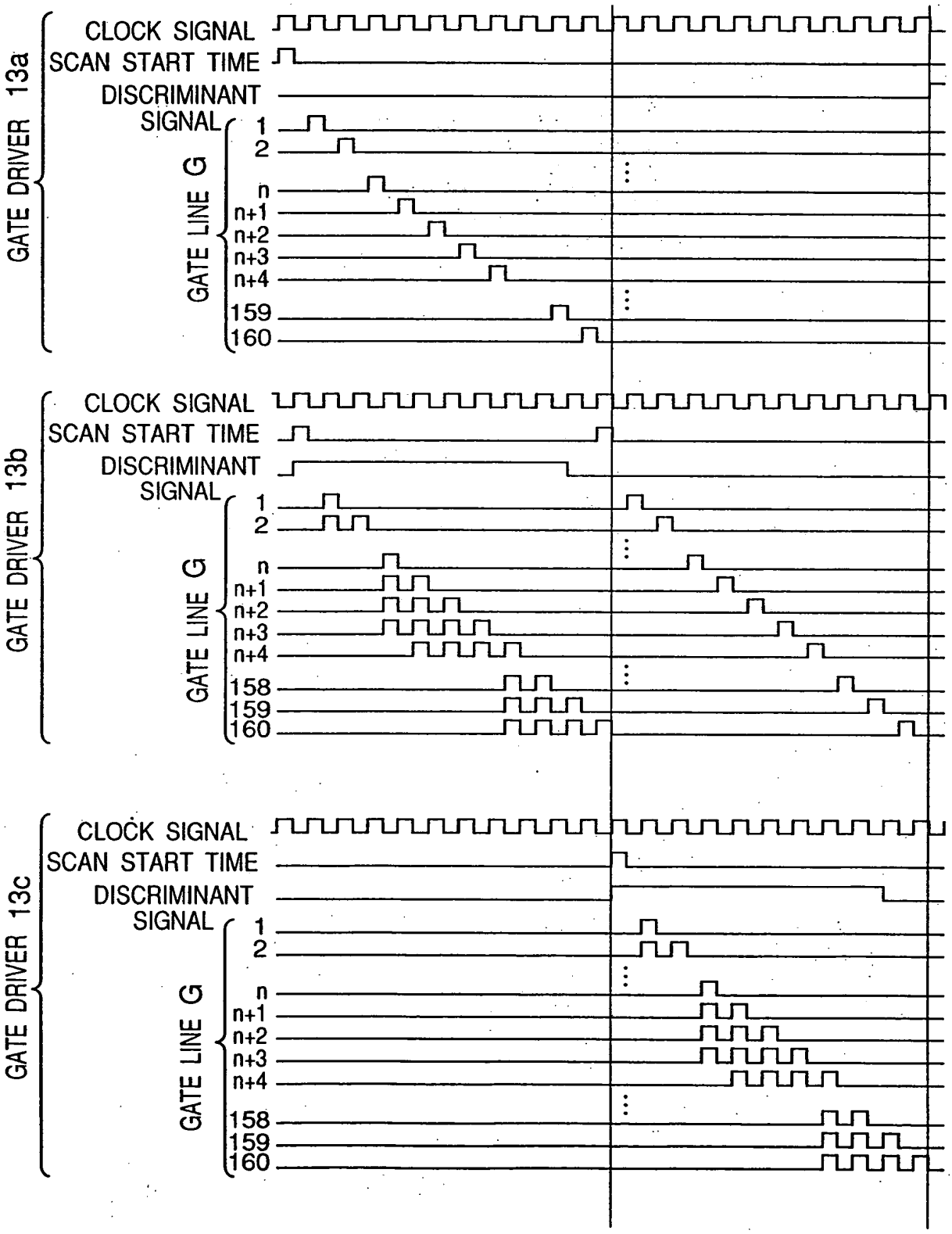


Fig.6

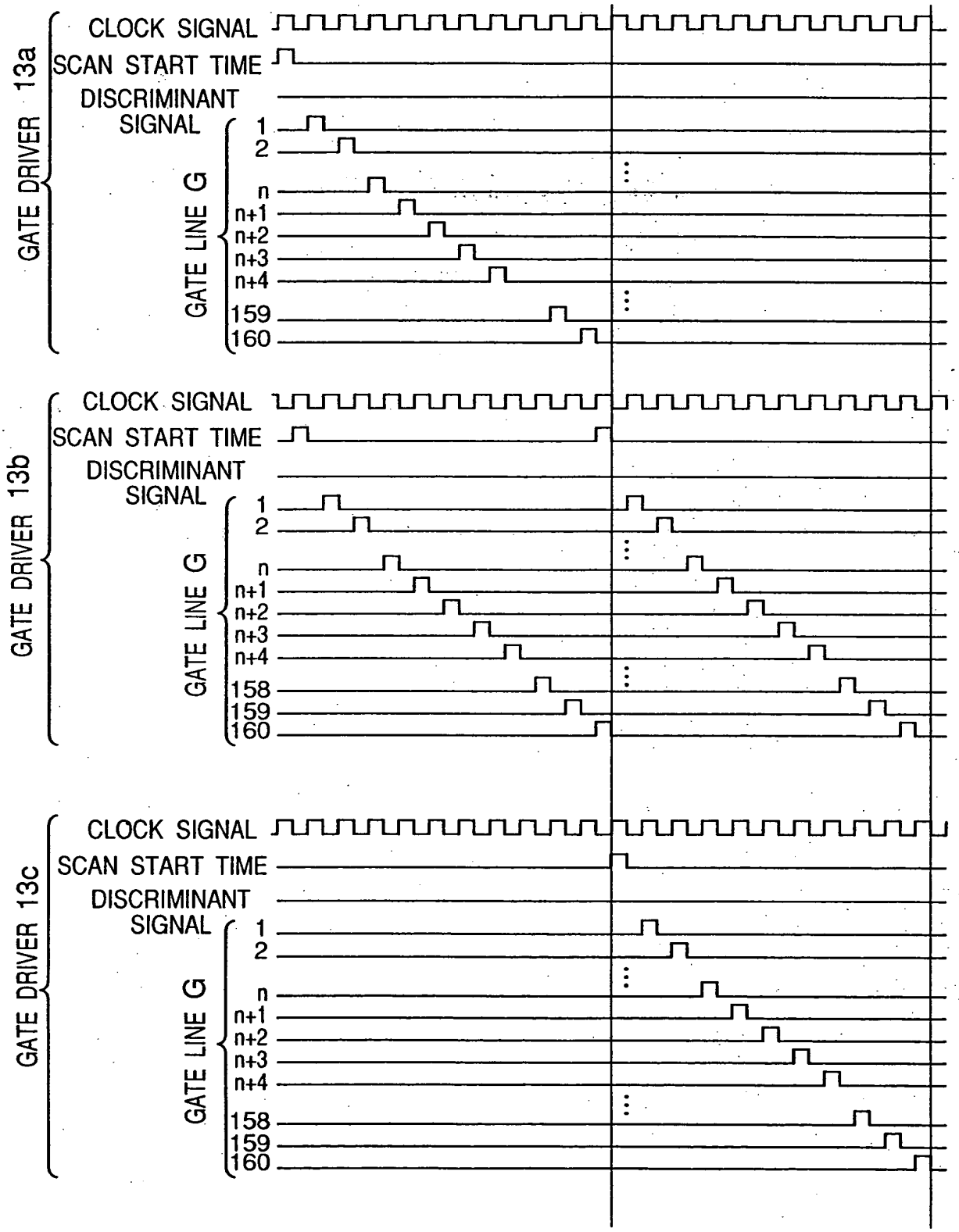


Fig.7

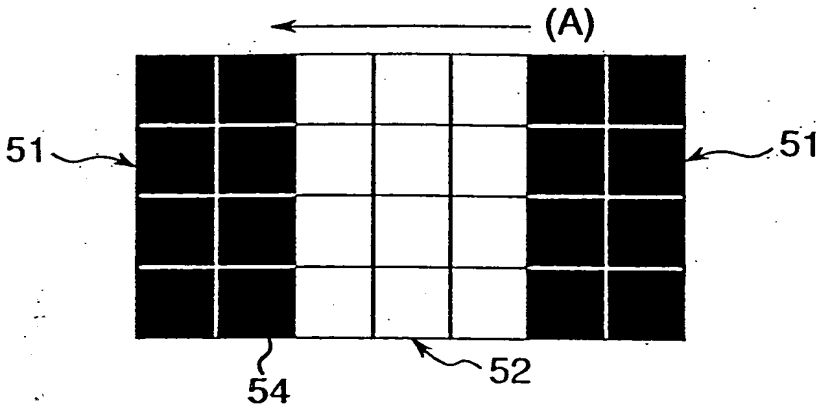


Fig.8

PRIOR ART

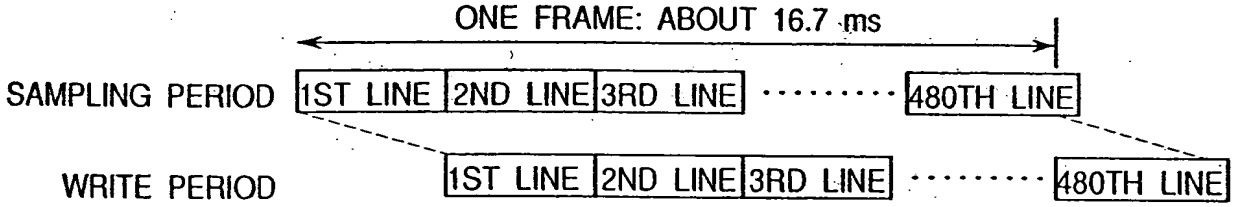


Fig.9

PRIOR ART

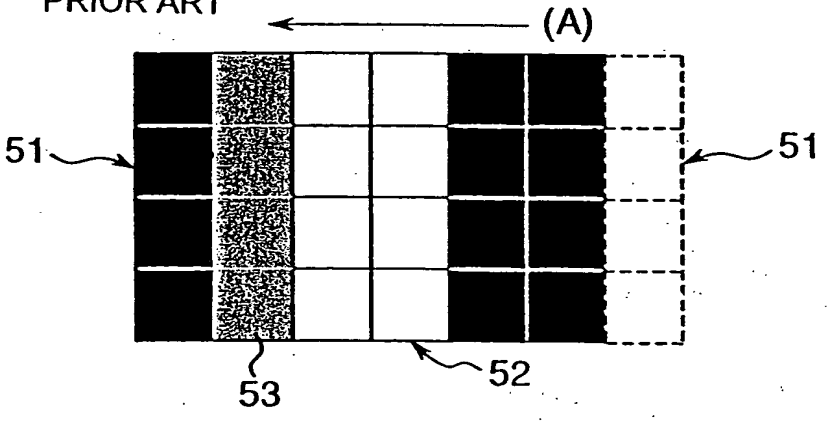


Fig.10
PRIOR ART

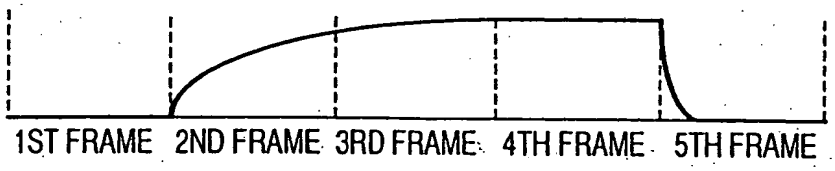


Fig.11A

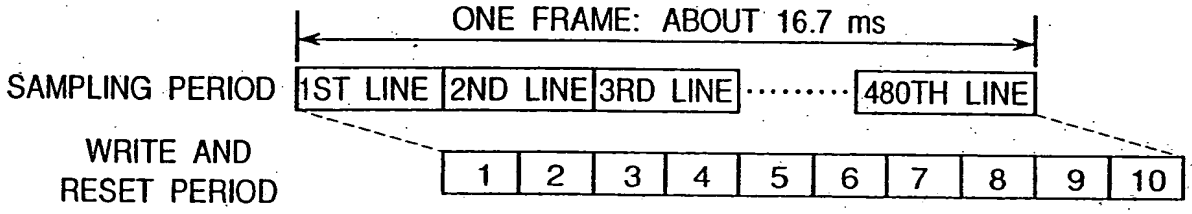


Fig.11B

NO.	STATE OF DRIVE
1	1ST LINE, DATA SIGNAL WRITE
2	161ST LINE, RESET SIGNAL WRITE
3	2ND LINE, DATA SIGNAL WRITE
4	162ND LINE, RESET SIGNAL WRITE
5	3RD LINE, DATA SIGNAL WRITE
6	163RD LINE, RESET SIGNAL WRITE
7	nTH LINE, DATA SIGNAL WRITE
8	(160+n)TH LINE, RESET SIGNAL WRITE
9	480TH LINE, DATA SIGNAL WRITE
10	160TH LINE, RESET SIGNAL WRITE

Fig.12

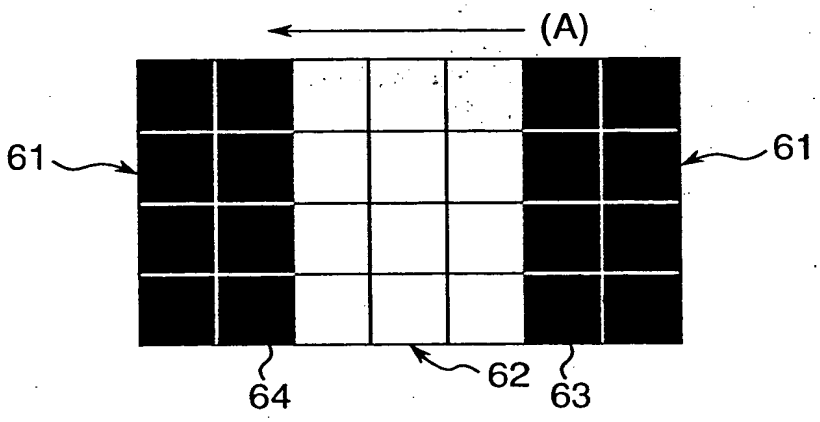


Fig.13

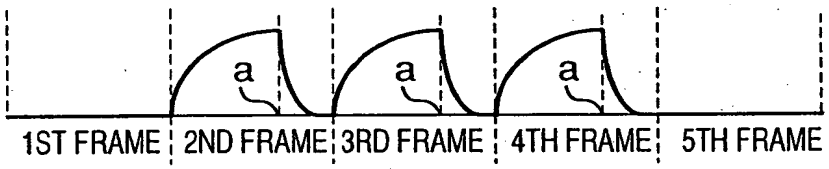


Fig.14

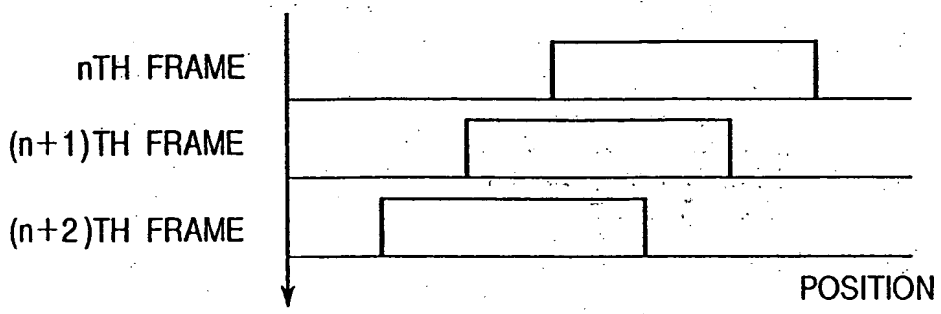


Fig.15
PRIOR ART

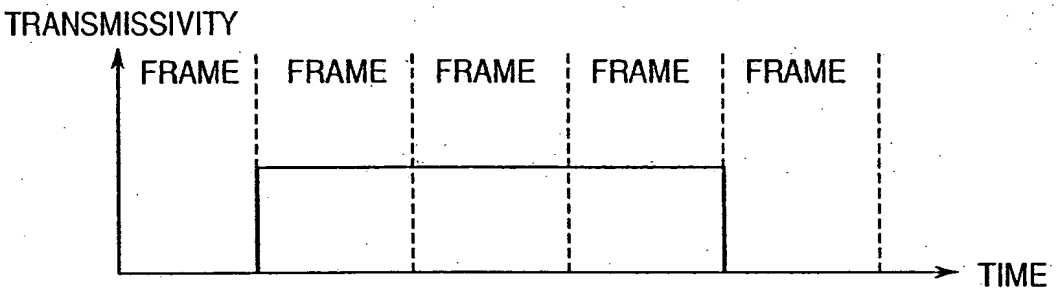


Fig.16

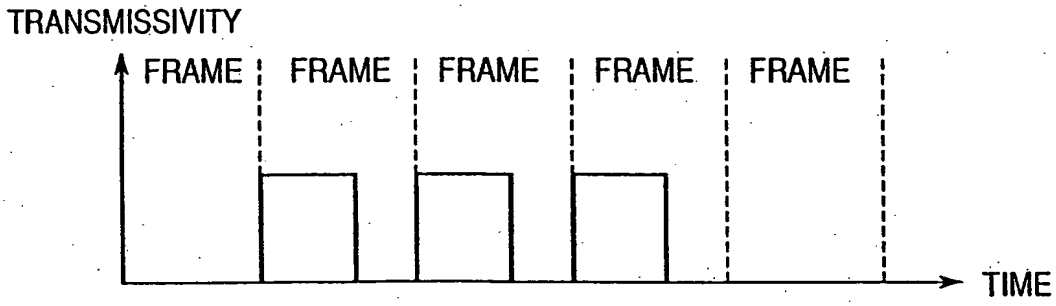


Fig.17

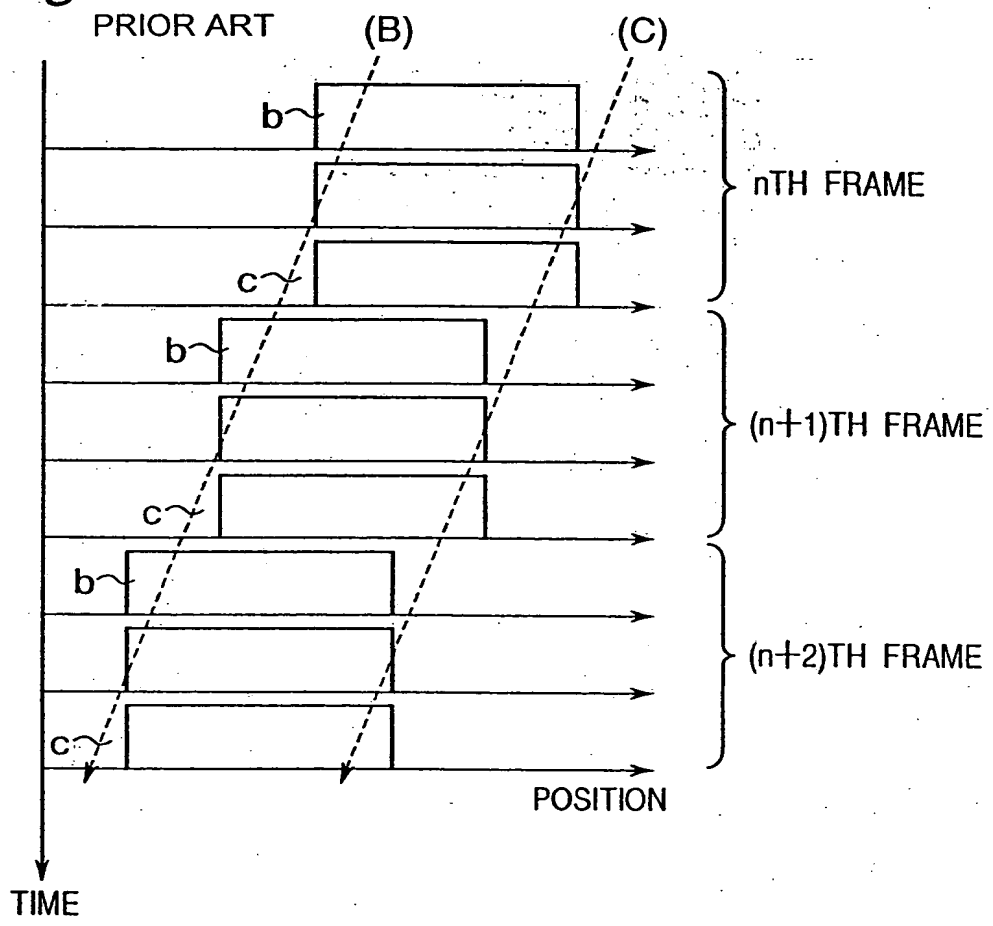


Fig.18

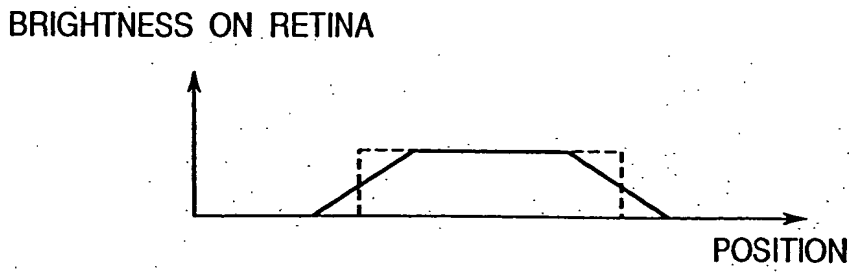


Fig.19

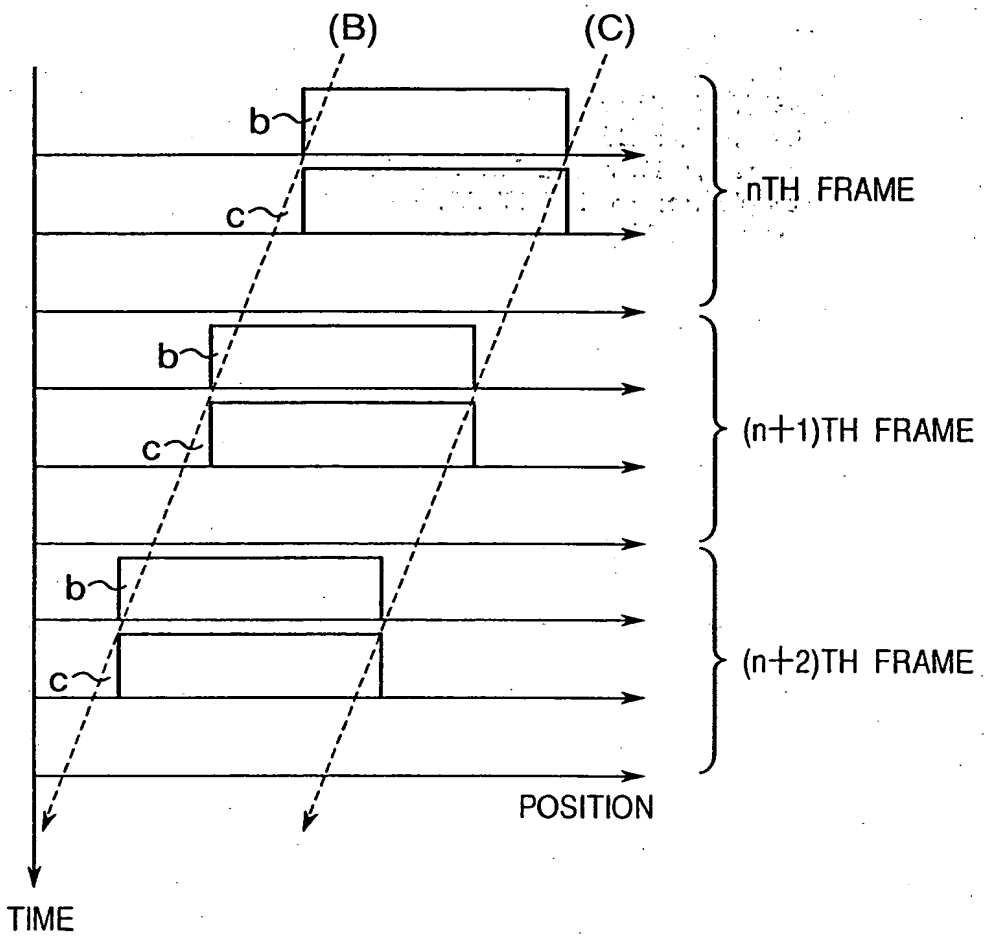


Fig.20

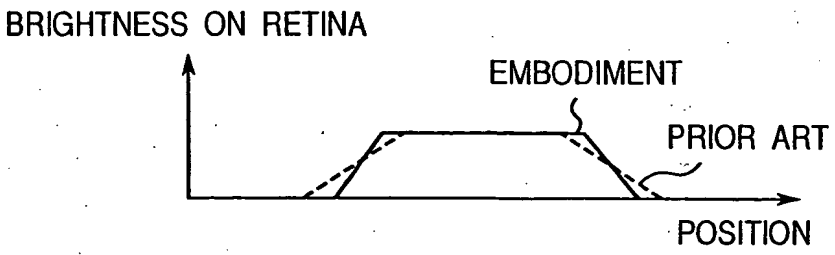


Fig.21

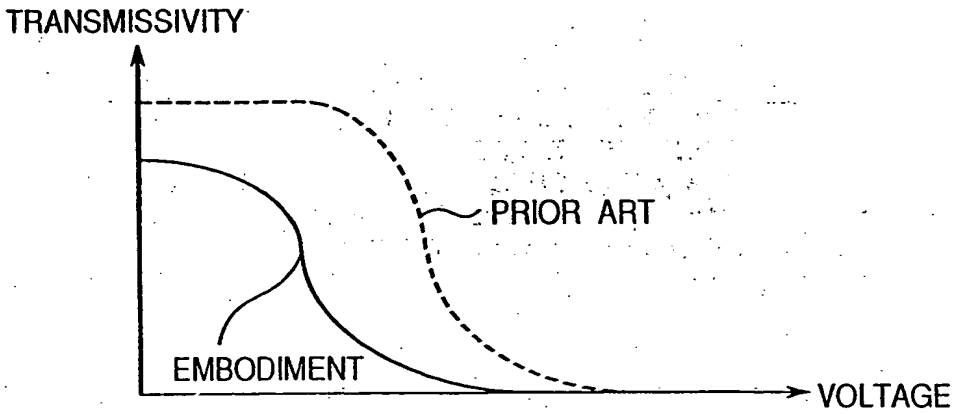


Fig.22A

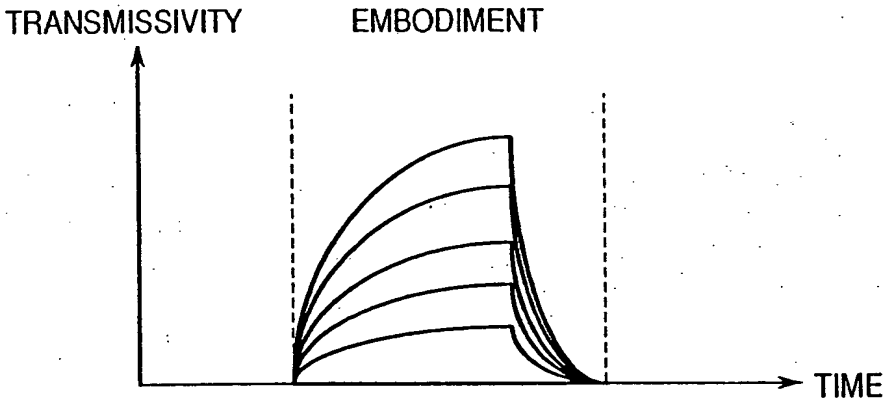


Fig.22B

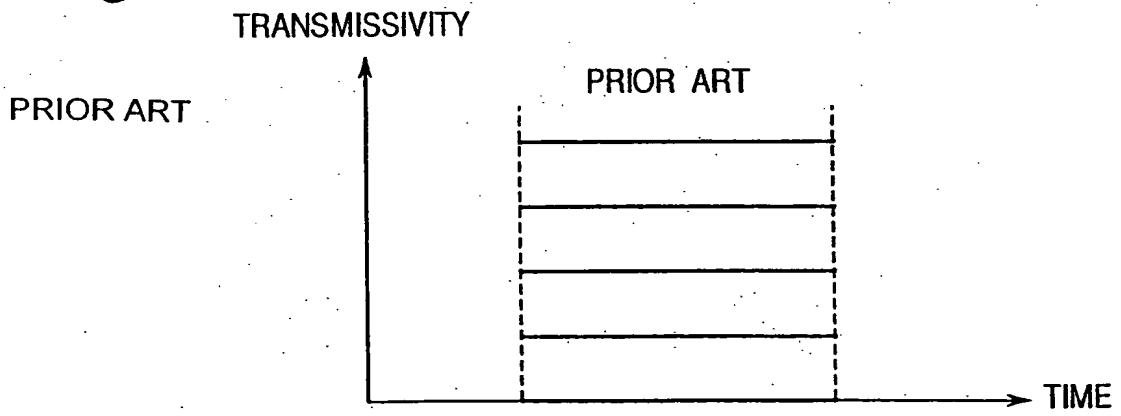


Fig.23

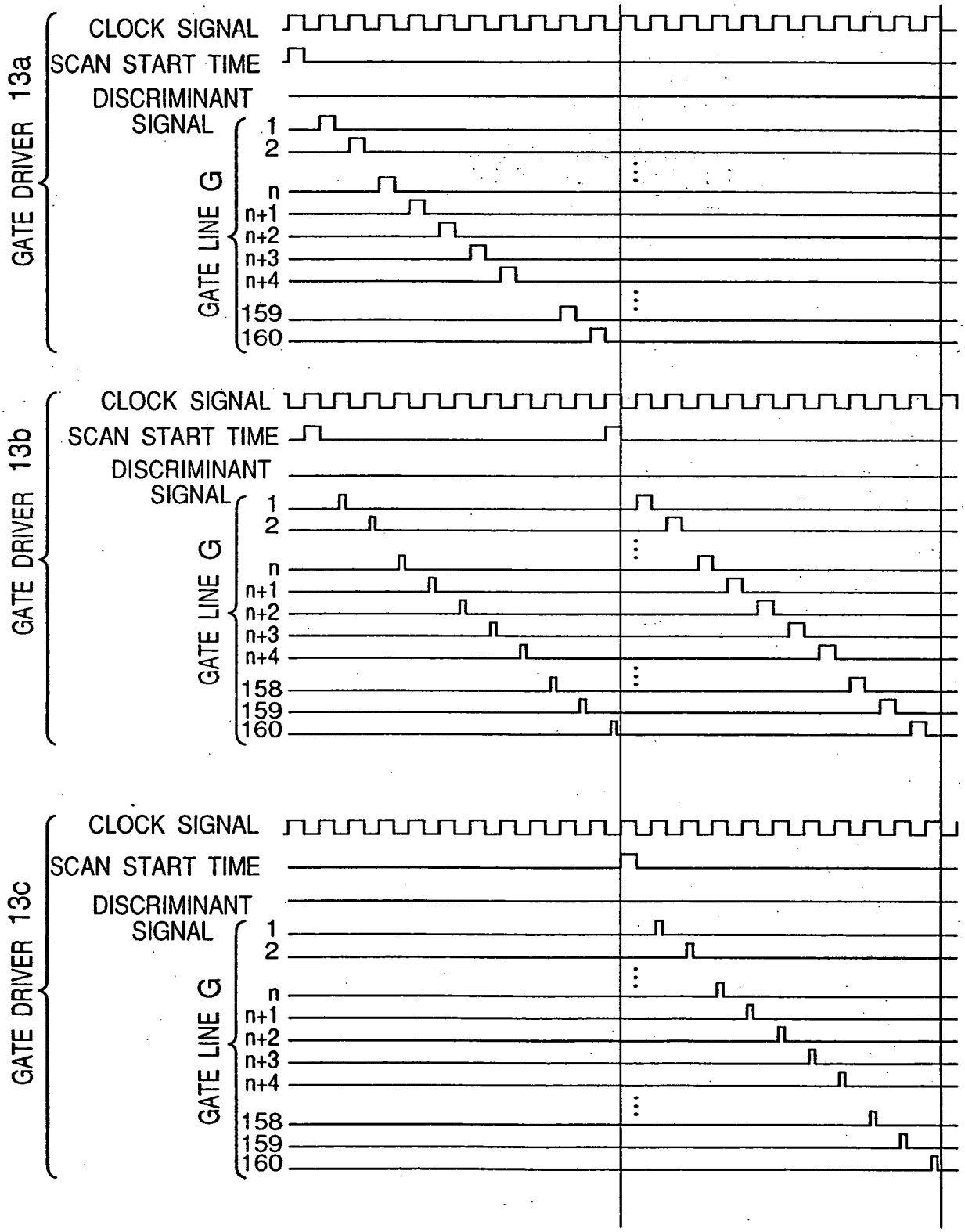


Fig.24

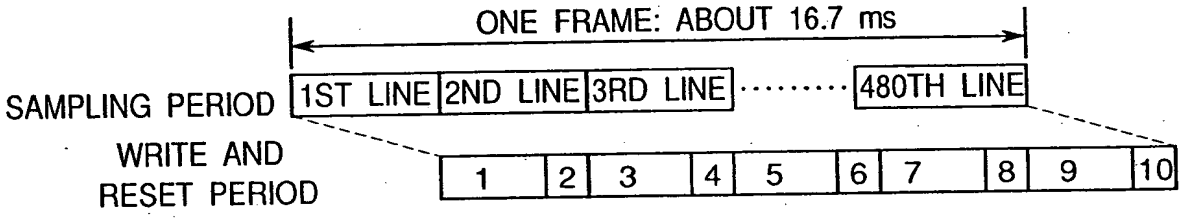
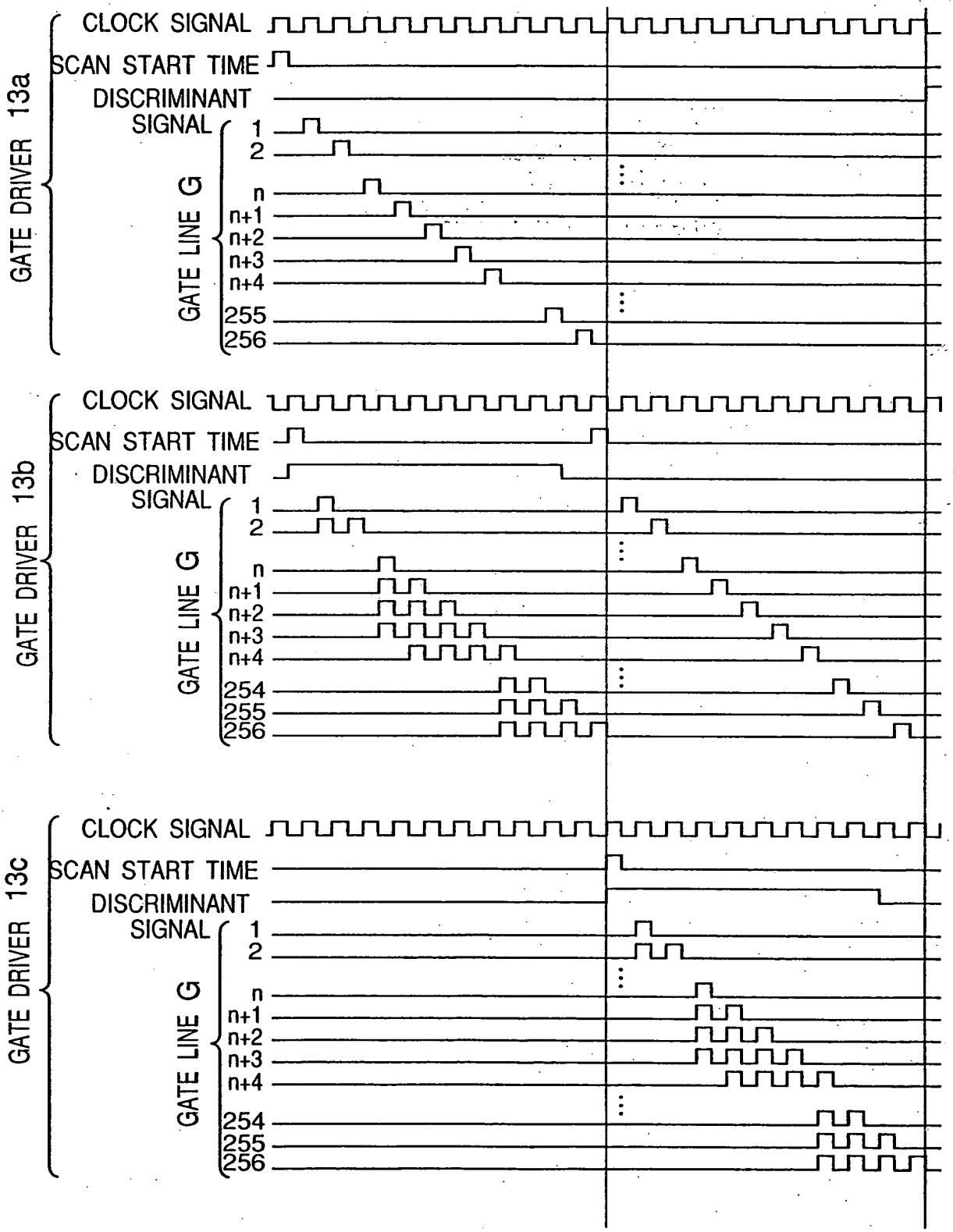


Fig.25



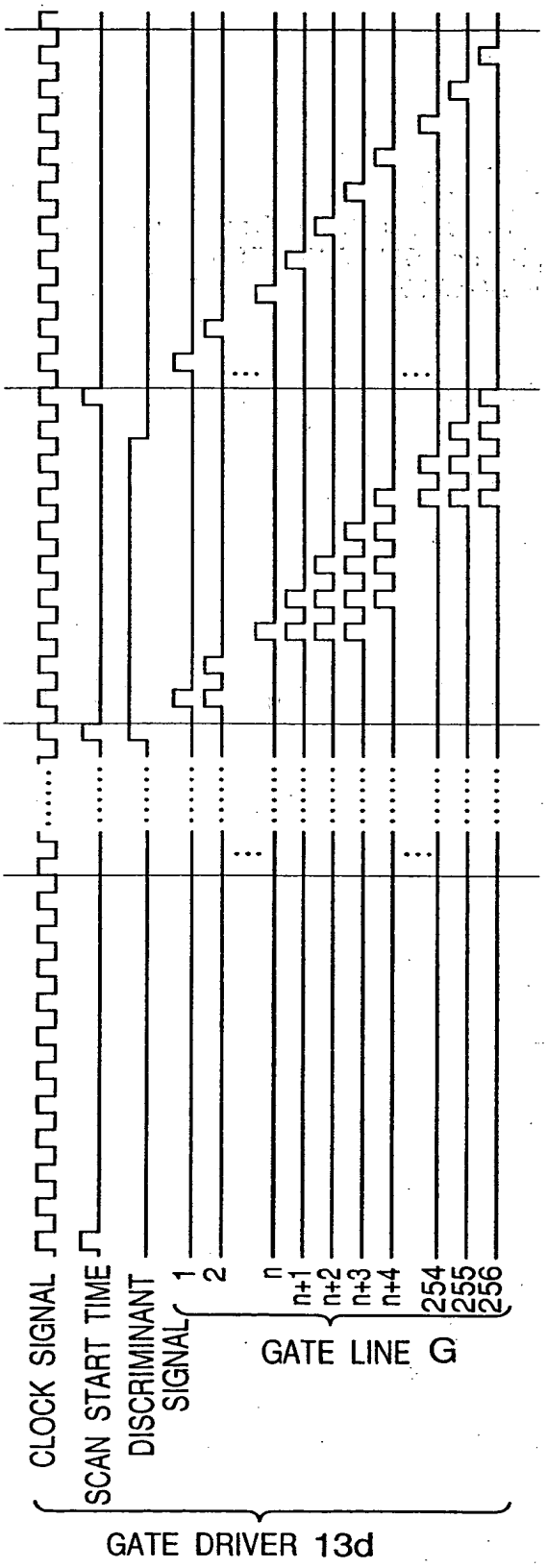


Fig.26

Fig.27A

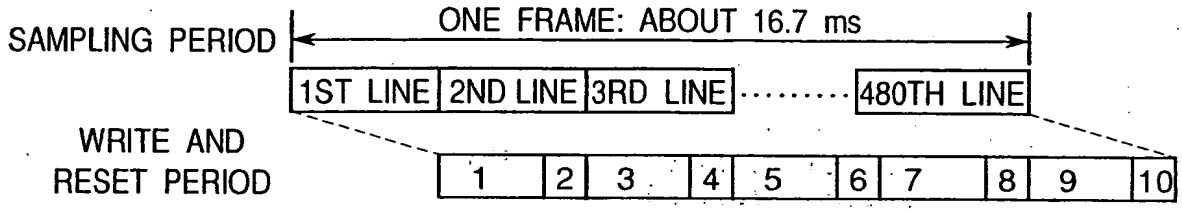


Fig.27B

NO.	STATE OF DRIVE
1	1ST LINE, DATA SIGNAL WRITE
2	257~260TH LINE, RESET SIGNAL WRITE
3	2ND LINE, DATA SIGNAL WRITE
4	258~261ST LINE, RESET SIGNAL WRITE
5	3RD LINE, DATA SIGNAL WRITE
6	259~262ND LINE, RESET SIGNAL WRITE
7	nTH LINE, DATA SIGNAL WRITE
8	(256+n)~(259+n)TH LINE, RESET SIGNAL WRITE
9	1024TH LINE, DATA SIGNAL WRITE
10	256~259TH LINE, RESET SIGNAL WRITE

Fig.28

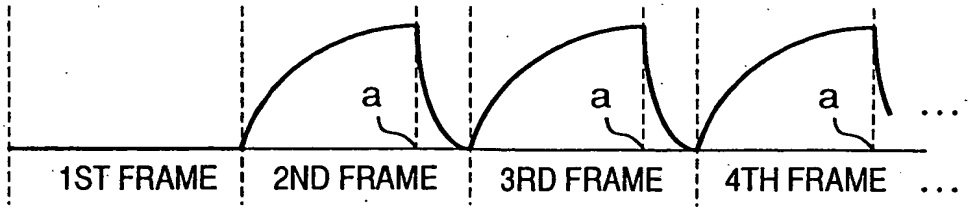


Fig.29

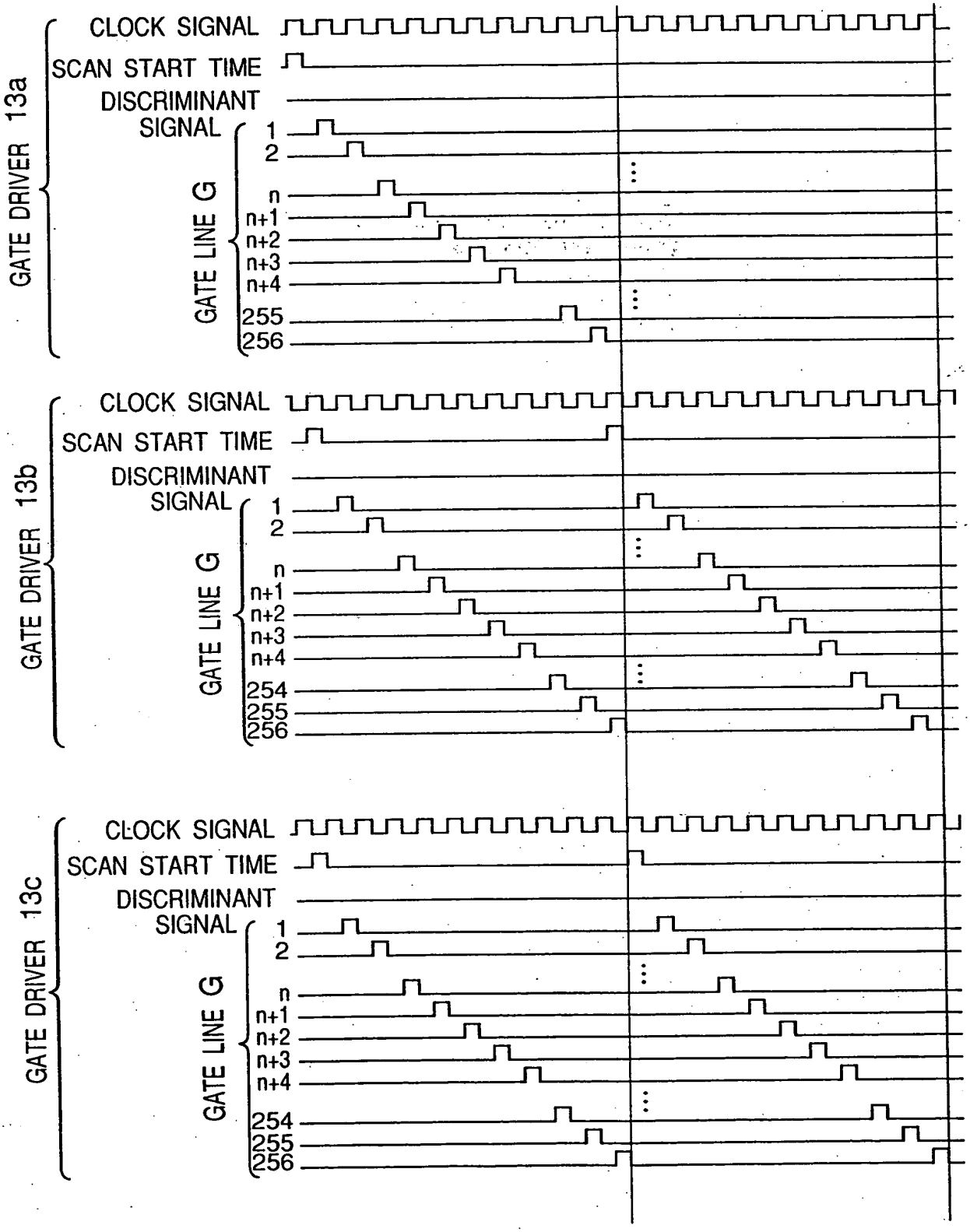


Fig.30A

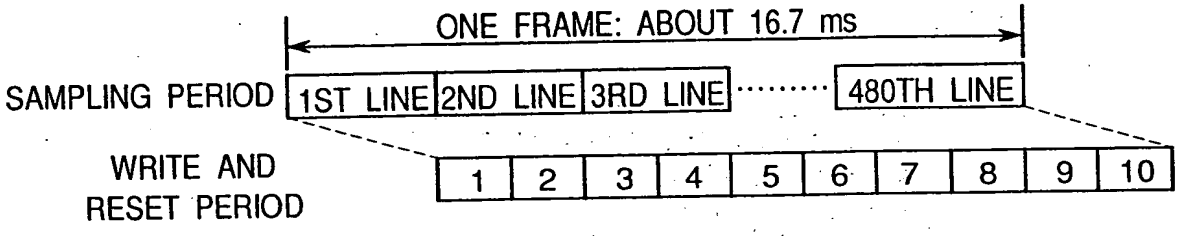


Fig.30B

NO.	STATE OF DRIVE
1	1ST LINE, DATA SIGNAL WRITE
2	257,513RD LINE, RESET SIGNAL WRITE
3	2ND LINE, DATA SIGNAL WRITE
4	258,514TH LINE, RESET SIGNAL WRITE
5	3RD LINE, DATA SIGNAL WRITE
6	259,515TH LINE, RESET SIGNAL WRITE
7	nTH LINE, DATA SIGNAL WRITE
8	(256+n),(512+n)TH LINE, RESET SIGNAL WRITE
9	480TH LINE, DATA SIGNAL WRITE
10	256,512TH LINE, RESET SIGNAL WRITE

Fig.31 PRIOR ART